



<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	10/081,767	
	Filing Date	February 25, 2002	
	First Named Inventor	Shunpei YAMAZAKI, et al.	
	Group Art Unit	2813	
	Examiner Name	Thanh T. Nguyen	
Total Number of Pages in This Submission	3	Attorney Docket Number	740756-2443
		Confirmation Number	9406

ENCLOSURES (check all that apply)		
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Signature	
Date	December 29, 2003

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
Shunpei YAMAZAKI, *et al.* ) Group Art Unit: 2813  
Serial No. 10/081,767 ) Examiner: Thanh T. Nguyen  
Filed: February 25, 2002 )  
For: METHOD OF MANUFACTURING A ) Confirmation No. 9406  
SEMICONDUCTOR FILM WITH )  
LITTLE WARP (AMENDED) )

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Sir:

**RESPONSE TO RESTRICTION/ELECTION  
OF SPECIE REQUIREMENT**

In response to the Restriction Requirement/Election of Species mailed December 15, 2003, the Applicants set forth the following election with traverse:

**Species 1, directed to Embodiments 5 and 6 (and Figures 21A-21D and Figures 22A-22E),**

**Accordingly, the invention of Specie I is readable on claims 6-8, 17-22, 49-77, 78-85.**

The Applicants additionally note for the Examiner's consideration that, notwithstanding the Examiner's improper identification that claims 6-8, 78 and 82 are specie (see MPEP Chapter 806.04(e)), claim 78, with its steps of "irradiating", "heating" and "etching", is generic to the invention of claims 7, 80 and 81 (which add the dependent features of crystallizing the semiconductor film prior to the irradiation step and/or promoting crystallization of the semiconductor film by adding a crystallization promoting metal to the semiconductor before the steps of "irradiating", "heating" and "etching" of claim 78). It is further noted that claim 82, with its steps


of “irradiating”, “etching” and “heating”, is generic to the invention of claims 8, 84 and 85 (which add the dependent features of crystallizing the semiconductor film prior to the irradiation step and/or promoting crystallization of the semiconductor film by adding a crystallization promoting metal to the semiconductor before the steps of “irradiating”, “etching” and “heating” of claim 82). Finally, it is also noted out that claim 6 is a sub-combination of either the combination claim 7 or claim 8, i.e., the “etching” step is omitted, and that combination claims 7 and 8 require the particulars of the sub-combination claim 6 (and are therefore not divisible therefrom according to MPEP Chapter 806.05(c)(II) – AB<sub>sp</sub>-B<sub>sp</sub> No Restriction).

Should the Examiner believe a conference would be of benefit in expediting the prosecution of the instant application, the Examiner is hereby invited to telephone counsel to arrange such a conference.

Consideration and allowance of the instant application is now respectfully requested.

Respectfully submitted,

By:

  
Jeffrey L. Costellia

Registration No. 35,483

Date: **December 29, 2003**

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